

First Named Inventor	Kirk D. Prall	SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT FORM PTO-1449
Serial No.	10/786,765	
Filing Date	February 25, 2004	
Group Art Unit	2818 2814	
Examiner Name	Unknown HOAI PHAM	
Confirmation No.	2857	
Attorney Docket No.	400.266US01	
Title: MULTI-LAYER MEMORY ARRAYS		Sheet 1 of 1

U.S. Patent References				
Examiner Initials	Document No.	Issue/Publication Date	Name	Filing Date
HP	2002/0079520 A1	06/27/2002	Nishihara	05/25/2001
I	2003/0214835 A1	11/20/2003	Nejad	05/16/2002
	5,091,762	02/25/1992	Watanabe	12/14/1990
HP	6,377,504 B1	04/23/2002	Hilbert	12/12/2000

Foreign Patent References					
Examiner Initials	Foreign Patent		Name	Publication Date	Translation
	Country	No.			
HP	EP	1 308 958 A2	Hewlett-Packard Company	05/07/2003	
HP	EP	1 318 522 A2	Hewlett-Packard Company	06/11/2003	

Other References	
Examiner Initials	Author, Title, Date, Pages, etc.

Examiner Signature	<i>Waylan</i>	Date Considered	01/05/06
*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			



First Named Inventor	Kirk D. Prall	INFORMATION DISCLOSURE STATEMENT FORM PTO-1449
Serial No.	10/786,765	
Filing Date	February 25, 2004	
Group Art Unit	2848 2814	
Examiner Name	Unknown HOAI PHAM	
Confirmation No.	2857	
Attorney Docket No.	400.266US01	
Title: MULTI-LAYER MEMORY ARRAYS		Sheet 1 of 1

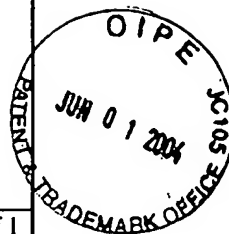
U.S. Patent References				
Examiner Initials	Document No.	Issue/Publication Date	Name	Filing Date
HP	6,034,882	03/07/2000	Johnson	11/16/1998
HP	6,735,104 B2	05/11/2004	Scheuerlein	05/16/2003
HP	6,822,903 B2	11/23/2004	Scheuerlein	03/31/2003

Foreign Patent References					
Examiner Initials	Foreign Patent		Name	Publication Date	Translation
	Country	No.			

Other References	
Examiner Initials	Author, Title, Date, Pages, etc.
HP	S. Herner et al., "Vertical p-I-n Polysilicon Diode With Antifuse for Stackable Field-Programmable ROM," IEEE Electron Device Letters, Vol. 25, No. 5, May 2004, pp. 271-273
HP	M. Johnson et al., "512-Mb PROM With a Three-Dimensional Array of Diode/Antifuse Memory Cells," IEEE Journal of Solid State Circuits, Vol. 38, No. 11, Nov. 2003, pp. 1920-1928
HP	F. Li et al., "Evaluation of SiO ₂ Antifuse in a 3D-OTP Memory," IEEE Transactions on Device and Materials Reliability, Vol. 4, No. 3, Sept. 2004, pp. 416-421

Examiner Signature	<i>Hoaylann</i>	Date Considered	01/05/06
*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			

First Named Inventor	Kirk D. Prall	INFORMATION DISCLOSURE STATEMENT FORM PTO-1449
Serial No.	10/786,765	
Filing Date	February 25, 2004	
Group Art Unit	2818 2814	
Examiner Name	Unknown HOAI PHAM	
Confirmation No.	2857	
Attorney Docket No.	400.266US01	
Title: MULTI-LAYER MEMORY ARRAYS		Sheet 1 of 1



U.S. Patent References				
Examiner Initials	Document No.	Issue/Publication Date	Name	Filing Date
HP	2003/0132458 A1	07/17/2003	Van Brocklin	01/16/2002
	2003/0224535 A1	12/04/2003	Andideh	05/31/2002
	2004/0026730 A1	02/12/2004	Kostylev	08/08/2002
	2004/0003191 A1	01/01/2004	Minne	06/28/2002
HP	6,657,250 B1	12/02/2003	Rudeck	08/21/2002

Foreign Patent References					
Examiner Initials	Foreign Patent		Name	Publication Date	Translation
	Country	No.			

Other References	
Examiner Initials	Author, Title, Date, Pages, etc.

Examiner Signature	<i>Waylan</i>	Date Considered	01/05/06
<small>*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>			